

## ABSTRACT OF THE DISCLOSURE

A method and device for hiding refresh operations during accesses to sub-arrays of a pseudo-static memory device. By refreshing sub-array<sub>j</sub> while filling the row R<sub>i</sub> (where  $i \neq j$ ) corresponding to sub-array<sub>j</sub>, refresh operations  
5 can be performed without risking that a read request will interrupt the refresh operation. Additional refresh operations of sub-array<sub>j</sub> can be performed serially with the operations of filling the row R<sub>i</sub> without delaying the overall burst read or write operation if the burst is made sufficiently long.

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